In industrial testing and research laboratories, it is essential to measure the voltages and currents accurately, ensuring perfect safety to the personnel and equipment. Hence a person handling the equipment as well as the metering devices must be protected against overvoltages and also against any induced voltages due to stray coupling. Therefore, the location and layout of the devices are important. Secondly, linear extrapolation of the devices beyond their ranges are not valid for high voltage meters and measuring instruments, and they have to be calibrated for the full range. Electromagnetic interference is a serious problem in impulse voltage and current measurements, and it has to be avoided or minimized. Therefore, even though the principles of measurements may be same, the devices and instruments for measurement of high voltages and currents differ vastly from the low voltage and low current devices. Different devices used for high voltage measurements may be classified as in Tables 7.1 and 7.2.

### 7.1 MEASUREMENT OF HIGH DIRECT CURRENT VOLTAGES

Measurement of high d.c. voltages as in low voltage measurements, is generally accomplished by extension of meter range with a large series resistance. The net current in the meter is usually limited to one to ten microamperes for full-scale deflection. For very high voltages (1000 kV or more) problems arise due to large power dissipation, leakage currents and limitation of voltage stress per unit length, change in resistance due to temperature variations, etc. Hence, a resistance potential divider with an electrostatic voltmeter is sometimes better when high precision is needed. But potential dividers also suffer from the disadvantages stated above. Both series resistance meters and potential dividers cause current drain from the source. Generating voltmeters are high impedance devices and do not load the source. They provide complete isolation from the source voltage (high voltage) as they are not directly connected to the high voltage terminal and hence are safer. Spark gaps such as sphere gaps are gas discharge devices and give an accurate measure of the peak voltage. These are quite simple and do not require any specialized construction. But the measurement is affected by the atmospheric conditions like temperature, humidity, etc. and by the vicinity of earthed objects, as the electric field in the gap is affected by the presence of earthed objects. But sphere gap measurement of voltages is independent of the waveform and frequency.
### Table 7.1 High Voltage Measurement Techniques

<table>
<thead>
<tr>
<th>Type of voltage</th>
<th>Method or technique</th>
</tr>
</thead>
<tbody>
<tr>
<td>(a) d.c. voltages</td>
<td><em>(i)</em> Series resistance microammeter</td>
</tr>
<tr>
<td></td>
<td><em>(ii)</em> Resistance potential divider</td>
</tr>
<tr>
<td></td>
<td><em>(iii)</em> Generating voltmeters</td>
</tr>
<tr>
<td></td>
<td><em>(iv)</em> Sphere and other spark gaps</td>
</tr>
<tr>
<td>(b) a.c. voltages</td>
<td><em>(i)</em> Series impedance ammeters</td>
</tr>
<tr>
<td>(power frequency)</td>
<td><em>(ii)</em> Potential dividers</td>
</tr>
<tr>
<td></td>
<td>(resistance or capacitance type)</td>
</tr>
<tr>
<td></td>
<td><em>(iii)</em> Potential transformers</td>
</tr>
<tr>
<td></td>
<td>(electromagnetic or CVT)</td>
</tr>
<tr>
<td></td>
<td><em>(iv)</em> Electrostatic voltmeters</td>
</tr>
<tr>
<td></td>
<td><em>(v)</em> Sphere gaps</td>
</tr>
<tr>
<td>(c) a.c. high frequency voltages, impulse voltages, and other rapidly changing voltages</td>
<td><em>(i)</em> Potential dividers with a cathode ray oscillograph (resistive or capacitive dividers)</td>
</tr>
<tr>
<td></td>
<td><em>(ii)</em> Peak voltmeters</td>
</tr>
<tr>
<td></td>
<td><em>(iii)</em> Sphere gaps</td>
</tr>
</tbody>
</table>

### Table 7.2 High Current Measurement Techniques

<table>
<thead>
<tr>
<th>Type of current</th>
<th>Device or technique</th>
</tr>
</thead>
<tbody>
<tr>
<td>(a) Direct currents</td>
<td><em>(i)</em> Resistive shunts with milliammeter</td>
</tr>
<tr>
<td></td>
<td><em>(ii)</em> Hall effect generators</td>
</tr>
<tr>
<td></td>
<td><em>(iii)</em> Magnetic links</td>
</tr>
<tr>
<td>(b) Alternating currents</td>
<td><em>(i)</em> Resistive shunts</td>
</tr>
<tr>
<td>(Power frequency)</td>
<td><em>(ii)</em> Electromagnetic current transformers</td>
</tr>
<tr>
<td>(c) High frequency a.c., impulse and rapidly changing currents</td>
<td><em>(i)</em> Resistive shunts</td>
</tr>
<tr>
<td></td>
<td><em>(ii)</em> Magnetic potentiometers or Rogowski coils</td>
</tr>
<tr>
<td></td>
<td><em>(iii)</em> Magnetic links</td>
</tr>
<tr>
<td></td>
<td><em>(iv)</em> Hall effect generators</td>
</tr>
</tbody>
</table>

#### 7.1.1 High Ohmic Series Resistance with Microammeter

High d.c. voltages are usually measured by connecting a very high resistance (few hundreds of megaohms) in series with a microammeter as shown in Fig. 7.1. Only the current $I$ flowing through the large calibrated resistance $R$ is measured by the moving coil microammeter. The voltage of the source is given by

$$V = IR$$
The voltage drop in the meter is negligible, as the impedance of the meter is only a few ohms compared to a few hundred mega-ohms of the series resistance $R$. A protective device like a paper gap, a neon glow tube, or a zener diode with a suitable series resistance is connected across the meter as a protection against high voltages in case the series resistance $R$ fails or flashes over. The ohmic value of the series resistance $R$ is chosen such that a current of one to ten microamperes is allowed for full-scale deflection. The resistance is constructed from a large number of wire wound resistors in series. The voltage drop in each resistor element is chosen to avoid surface flashovers and discharges. A value of less than 5 kV/cm in air or less than 20 kV/cm in good oil is permissible. The resistor chain is provided with corona free terminations. The material for resistive elements is usually a carbon-alloy with temperature coefficient less than $10^{-4/°C}$. Carbon and other metallic film resistors are also used. A resistance chain built with ±1% carbon resistors located in an airtight transformer oil filled P.V.C. tube, for 100 kV operation had very good temperature stability. The limitations in the series resistance design are:

(i) power dissipation and source loading,
(ii) temperature effects and long time stability,
(iii) voltage dependence of resistive elements, and
(iv) sensitivity to mechanical stresses.

Series resistance meters are built for 500 kV d.c. with an accuracy better than 0.2%.

7.1.2 Resistance Potential Dividers for d.c. Voltages

A resistance potential divider with an electrostatic or high impedance voltmeter is shown in Fig. 7.2. The influence of temperature and voltage on the elements is eliminated in the voltage divider arrangement. The high voltage magnitude is given by $[(R_1 + R_2)/R_2]v_2$, where $v_2$ is the d.c. voltage across the low voltage arm $R_2$. With sudden changes in voltage, such as switching operations, flashover of the test objects, or source short circuits, flashover or damage may occur to the divider elements due to the stray capacitance across the elements and due to ground capacitances. To avoid these transient voltages, voltage controlling capacitors are connected across the elements. A corona free termination is also necessary to avoid unnecessary discharges at high voltage ends. A series resistor with a parallel capacitor connection for linearization of transient potential distribution is shown in Fig. 7.3. Potential dividers are made with 0.05% accuracy up to 100 kV, with 0.1% accuracy up to 300 kV, and with better than 0.5% accuracy for 500 kV.
7.1.3 Generating Voltmeters

High voltage measuring devices employ generating principle when source loading is prohibited (as with Van de Graaff generators, etc.) or when direct connection to the high voltage source is to be avoided. A generating voltmeter is a variable capacitor electrostatic voltage generator which generates current proportional to the applied external voltage. The device is driven by an external synchronous or constant speed motor and does not absorb power or energy from the voltage measuring source.

Principle of Operation

The charge stored in a capacitor of capacitance $C$ is given by $q = CV$. If the capacitance of the capacitor varies with time when connected to the source of voltage $V$, the current through the capacitor,

$$ i = \frac{dq}{dt} = V \frac{dC}{dt} + C \frac{dV}{dt} $$

(7.1)

For d.c. voltages $dV/dt = 0$. Hence,

$$ i = \frac{dq}{dt} = V \frac{dC}{dt} $$

(7.2)

If the capacitance $C$ varies between the limits $C_0$ and $(C_0 + C_m)$ sinusoidally as

$$ C = C_0 + C_m \sin \omega t $$

the current $i$ is

$$ i = i_m \cos \omega t $$

where

$$ i_m = V C_m \omega $$
(\(i_m\) is the peak value of the current). The rms value of the current is given by:

\[
i_{\text{rms}} = \frac{VC_m\omega}{\sqrt{2}}
\]  

(7.3)

For a constant angular frequency \(\omega\), the current is proportional to the applied voltage \(V\). More often, the generated current is rectified and measured by a moving coil meter. Generating voltmeters can be used for a.c. voltage measurements also provided the angular frequency \(\omega\) is the same or equal to half that of the supply frequency.

A generating voltmeter with a rotating cylinder consists of two exciting field electrodes and a rotating two pole armature driven by a synchronous motor at a constant speed \(n\). The a.c. current flowing between the two halves of the armature is rectified by a commutator whose arithmetic mean may be calculated from:

\[
i = \frac{n}{30} \Delta C V, \quad \text{where } \Delta C = C_{\text{max}} - C_{\text{min}}
\]

For a symmetric voltage \(C_{\text{min}} = 0\). When the voltage is not symmetrical, one of the electrodes is grounded and \(C_{\text{min}}\) has a finite value. The factor of proportionality \(\frac{n}{30} \cdot \Delta C\) is determined by calibration.

This device can be used for measuring a.c. voltages provided the speed of the drive-motor is half the frequency of the voltage to be measured. Thus a four-pole synchronous motor with 1500 rpm is suitable for 50 Hz. For peak value measurements, the phase angle of the motor must also be so adjusted that \(C_{\text{max}}\) and the crest value occur at the same instant.

Generating voltmeters employ rotating sectors or vanes for variation of capacitance. Figure 7.4 gives a schematic diagram of a generating voltmeter. The high voltage source is connected to a disc electrode \(S_3\) which is kept at a fixed distance on the axis of the other low voltage electrodes \(S_0, S_1,\) and \(S_2\). The rotor \(S_0\) is driven at a constant speed by a synchronous motor at a suitable speed (1500, 1800, 3000, or 3600 rpm). The rotor vanes of \(S_0\) cause periodic change in capacitance between the insulated disc \(S_2\) and the h.v. electrode \(S_3\). The shape and number of the vanes of \(S_0\) and \(S_1\) are so designed that they produce sinusoidal variation in the capacitance. The generated a.c. current through the resistance \(R\) is rectified and read by a moving coil instrument. An amplifier is needed, if the shunt capacitance is large or longer leads are used for connection to rectifier and meter. The instrument is calibrated using a potential divider or sphere gap. The meter scale is linear and its range can be extended.

![Schematic diagram of a generating voltmeter (rotating vane type)](image-url)
Advantages of Generating Voltmeters

(i) No source loading by the meter,
(ii) no direct connection to high voltage electrode,
(iii) scale is linear and extension of range is easy, and
(iv) a very convenient instrument for electrostatic devices such as Van de Graaff generator and particle accelerators.

Limitations of Generating Voltmeters

(i) They require calibration,
(ii) careful construction is needed and is a cumbersome instrument requiring an auxiliary drive, and
(iii) disturbance in position and mounting of the electrodes make the calibration invalid.

7.1.4 Other Methods—Oscillating Spheroid

The period of oscillation of an oscillating spheroid in a uniform electric field is proportional to the applied electric field. This principle is made use of in measuring high d.c. voltages. The period of oscillation of a suspended spheroid between two electrodes with and without an electric field present is measured. If the frequency of the oscillation for small amplitudes is \( f \) and \( f_0 \) respectively, then the electric field

\[
E \propto \left( f^2 - f_0^2 \right)^{-\frac{1}{2}}
\]

and hence the applied voltage

\[
V \propto \left( f^2 - f_0^2 \right)^{-\frac{1}{2}}
\]  

(7.4)
since \( E = V/d \) (\( d \) being the gap separation between the electrodes). The proportionality constant can be determined from the dimensions of the spheroid or experimentally.

The uniform electric field is produced by employing two electrodes with a Bruce profile for a spacing of about 50 cm. One of the electrodes is earthed and the other is connected to a high voltage d.c. source. The spheroid is suspended at the centre of the electrodes in the axis of the electric field. The period of oscillation is measured using a telescope and stop watch. Instruments of this type are constructed for voltages up to 200 kV, and the accuracy is estimated to be ±0.1%. In Bruce’s design, electrodes of 145 cm diameter with 45 cm spacing were used. An overall accuracy of ±0.03% was claimed up to a maximum voltage of 250 kV. Since this is a very complicated and time consuming method, it is not widely used. The useful range of the spheroidal voltmeter is limited by local discharges.

7.1.5 Measurement of Ripple Voltage in d.c. Systems

It has been discussed in the previous chapter that d.c. rectifier circuits contain ripple, which should be kept low (<< 3%). Ripple voltages are a.c. voltages of non-sinusoidal nature, and as such oscillographic measurement of these voltages is desirable. However, if a resistance potential divider is used along with an oscilloscope, the measurement of small values of the ripple \( \delta V \) will be inaccurate.

A simple method of measuring the ripple voltage is to use a capacitance-resistance (C-R) circuit and measure the varying component of the a.c. voltage by blocking the d.c. component. If \( V_1 \) is the d.c. source voltage with ripple (Fig. 7.6a) and \( V_2 \) is the voltage across the measuring resistance \( R \), with \( C \) acting as the blocking capacitor, then

\[
V_2(t) = V_1(t) - V_{d.c.} = \text{ripple voltage}
\]

The condition to be satisfied here is \( \omega CR >> 1 \).

Measurement of Ripple with CRO

The detailed circuit arrangement used for this purpose is shown in Fig. 7.6b. Here, the capacitance ‘\( C \)’ is rated for the peak voltage. It is important that the switch ‘\( S \)’ be closed when the CRO is connected to the source so that the CRO input terminal does not receive any high voltage signal while ‘\( C \)’ is being charged. Further, \( C \) should be

---

![Fig. 7.6](image-url) Circuit arrangement for the measurement of ripple voltage
larger than the capacitance of the cable and the input capacitance of the CRO, taken together.

7.2 MEASUREMENT OF HIGH a.c. AND IMPULSE VOLTAGES: INTRODUCTION

Measurement of high a.c. voltages employ conventional methods like series impedance voltmeters, potential dividers, potential transformers, or electrostatic voltmeters. But their designs are different from those of low voltage meters, as the insulation design and source loading are the important criteria. When only peak value measurement is needed, peak voltmeters and sphere gaps can be used. Often, sphere gaps are used for calibration purposes. Impulse and high frequency a.c. measurements invariably use potential dividers with a cathode ray oscillograph for recording voltage waveforms. Sphere gaps are used when peak values of the voltage are only needed and also for calibration purposes.

7.2.1 Series Impedance Voltmeters

For power frequency a.c. measurements the series impedance may be a pure resistance or a reactance. Since resistances involve power losses, often a capacitor is preferred as a series reactance. Moreover, for high resistances, the variation of resistance with temperature is a problem, and the residual inductance of the resistance gives rise to an impedance different from its ohmic resistance. High resistance units for high voltages have stray capacitances and hence a unit resistance will have an equivalent circuit as shown in Fig. 7.7. At any frequency \( \omega \) of the a.c. voltage, the impedance of the resistance \( R \) is

\[
Z = \frac{R + j \omega L}{(1 - \omega^2 LC) + j \omega CR}
\]  

\( (7.5) \)

![Fig. 7.7 Simplified lumped parameter equivalent circuit of a high ohmic resistance \( R \)]

- \( L \) — Residual inductance
- \( C \) — Residual capacitance

If \( \omega L \) and \( \omega C \) are small compared to \( R \),

\[
Z \approx R \left[ 1 + j \left( \frac{\omega L}{R} - \omega CR \right) \right]
\]  

\( (7.6) \)

and the total phase angle is
\[
\varphi \approx \left( \frac{\omega L}{R} - \omega CR \right)
\]  
(7.7)

This can be made zero and independent of frequency, if

\[ \frac{L}{C} = R^2 \]  
(7.8)

For extended and large dimensioned resistors, this equivalent circuit is not valid and each elemental resistor has to be approximated with this equivalent circuit. The entire resistor unit then has to be taken as a transmission line equivalent, for calculating the effective resistance. Also, the ground or stray capacitance of each element influences the current flowing in the unit, and the indication of the meter results in an error. The equivalent circuit of a high voltage resistor neglecting inductance and the circuit of compensated series resistor using guard and tuning resistors is shown in Figs. 7.8a and b respectively. Stray ground capacitance effects (refer Fig. 7.8b) can be removed by shielding the resistor \( R \) by a second surrounding spiral \( R_s \), which shunts the actual resistor but does not contribute to the current through the instrument. By tuning the resistors \( R_a \), the shielding resistor end potentials may be adjusted with respect to the actual measuring resistor so that the resulting compensation currents between the shield and the measuring resistors provide a minimum phase angle.

\[\text{(a) Extended series resistance with inductance neglected}\]
\[\text{(b) Series resistance with guard and tuning resistances}\]

- \( C_g \) — Stray capacitance to ground
- \( C_s \) — Winding capacitance
- \( R \) — Series resistor
- \( R_s \) — Guard resistor
- \( R_a \) — Tuning resistor

**Fig. 7.8** Extended series resistance for high a.c. voltage measurements
**Series Capacitance Voltmeter**

To avoid the drawbacks pointed out earlier, a series capacitor is used instead of a resistor for a.c. high voltage measurements. The schematic diagram is shown in Fig. 7.9. The current $I_c$ through the meter is:

$$I_c = j \omega CV$$  \hspace{1cm} (7.9)

![Schematic diagram](image)

where, $C =$ capacitance of the series capacitor,  
$\omega =$ angular frequency, and  
$V =$ applied a.c. voltage.

If the a.c. voltage contains harmonics, error due to changes in series impedance occurs. The rms value of the voltage $V$ with harmonics is given by

$$V = \sqrt{V_1^2 + V_2^2 + \ldots + V_n^2}$$  \hspace{1cm} (7.10)

where $V_1, V_2, \ldots, V_n$ represent the rms value of the fundamental, second ... and $n$th harmonics.

The currents due to these harmonics are

$$I_1 = \omega CV_1$$
$$I_2 = 2 \omega CV_2, \ldots$$
$$I_n = n \omega CV_n$$  \hspace{1cm} (7.11)

Hence, the resultant rms current is:

$$I = \omega C (V_1^2 + 4V_2^2 + \ldots + n^2V_n^2)^{1/2}$$  \hspace{1cm} (7.12)

With a 10% fifth harmonic only, the current is 11.2% higher, and hence the error is 11.2% in the voltage measurement.

This method is not recommended when a.c. voltages are not pure sinusoidal waves but contain considerable harmonics.

Series capacitance voltmeters were used with cascade transformers for measuring rms values up to 1000 kV. The series capacitance was formed as a parallel plate capacitor between the high voltage terminal of the transformer and a ground plate suspended above it. A rectifier ammeter was used as an indicating instrument and was directly calibrated in high voltage rms value. The meter was usually a 0-100 µA moving coil meter and the overall error was about 2%.

### 7.2.2 Capacitance Potential Dividers and Capacitance Voltage Transformers

The errors due to harmonic voltages can be eliminated by the use of capacitive voltage dividers with an electrostatic voltmeter or a high impedance meter such as a V.T.V.M. If the meter is connected through a long cable, its capacitance has to be
taken into account in calibration. Usually, a standard compressed air or gas condenser is used as $C_1$ (Fig. 7.10), and $C_2$ may be any large capacitor (mica, paper, or any low loss condenser). $C_1$ is a three terminal capacitor and is connected to $C_2$ through a shielded cable, and $C_2$ is completely shielded in a box to avoid stray capacitances. The applied voltage $V_1$ is given by

$$V_1 = V_2 \left( \frac{C_1 + C_2 + C_m}{C_1} \right) \quad (7.13)$$

where $C_m$ is the capacitance of the meter and the connecting cable and the leads and $V_2$ is the meter reading.

**Capacitance Voltage Transformer—CVT**

Capacitance divider with a suitable matching or isolating potential transformer tuned for resonance condition is often used in power systems for voltage measurements. This is often referred to as CVT. In contrast to simple capacitance divider which requires a high impedance meter like a V.T.V.M. or an electrostatic voltmeter, a CVT can be connected to a low impedance device like a wattmeter pressure coil or a relay coil. CVT can supply a load of a few VA. The schematic diagram of a CVT with its equivalent circuit is given in Fig. 7.11. $C_1$ is made of a few units of high voltage condensers, and the total capacitance will be around a few thousand picofarads as against a gas filled standard condenser of about 100 pF. A matching transformer is connected between the load or meter $M$ and $C_2$. The transformer ratio is chosen on economic grounds, and the h.v. winding rating may be 10 to 30 kV with the l.v. winding rated from 100 to 500 V. The value of the tuning choke $L$ is chosen to make the equivalent circuit of the CVT purely resistive or to bring resonance condition. This condition is satisfied when

$$\omega (L + L_T) = \frac{1}{\omega(C_1 + C_2)} \quad (7.14)$$

where,

$L = \text{inductance of the choke, and}$

$L_T = \text{equivalent inductance of the transformer referred to h.v. side.}$

The voltage $V_2$ (meter voltage) will be in phase with the input voltage $V_1$.

The phasor diagram of CVT under resonant conditions is shown in Fig. 7.11. The meter is taken as a resistive load, and $X_m'$ is neglected. The voltage across the load referred to the divider side will be $V_2' = (I'M'R_m')$ and $V_{C_2} = V_2' + I_m(R_e + X_e)$. It is clear from the phasor diagram that $V_1$ (input voltage) = $(V_{C_1} + V_{C_2})$ and is in phase.
with $V'_2$, the voltage across the meter. $R_e$ and $X_e$ include the potential transformer resistance and leakage reactance. Under this condition, the voltage ratio becomes

$$a = \frac{V_1}{V_2} = \frac{V_{C_1} + V_{R_i} + V_2'}{V_2'} \quad (7.15)$$

(neglecting the voltage drop $I_m \cdot X_e$ which is very small compared to the voltage $V_{C_1}$)

where $V_{R_i}$ is the voltage drop in the transformer and choke windings.

The advantages of a CVT are:

(i) simple design and easy installation,

(ii) can be used both as a voltage measuring device for meter and relaying purposes and also as a coupling condenser for power line carrier communication and relaying.

(iii) frequency independent voltage distribution along elements as against conventional magnetic potential transformers which require additional insulation design against surges, and

(iv) provides isolation between the high voltage terminal and low voltage metering.

The disadvantages of a CVT are:

(i) the voltage ratio is susceptible to temperature variations, and

(ii) the problem of inducing ferro-resonance in power systems.

**Resistance Potential Dividers**

Resistance potential dividers suffer from the same disadvantages as series resistance voltmeters for a.c. applications. Moreover, stray capacitances and inductances (Figs 7.7 and 7.8) associated with the resistances make them inaccurate, and compensation has to be provided. Hence, they are not generally used.
7.2.3 Potential Transformers  
(Magnetic Type)

Magnetic potential transformers are the oldest devices for a.c. measurements. They are simple in construction and can be designed for any voltage. For very high voltages, cascading of the transformers is possible. The voltage ratio is:

$$\frac{V_1}{V_2} = a = \frac{N_1}{N_2} \quad (7.16)$$

where $V_1$ and $V_2$ are the primary and secondary voltages, and $N_1$ and $N_2$ are the respective turns in the windings.

These devices suffer from the ratio and phase angle errors caused by the magnetizing and leakage impedances of the transformer windings. The errors are compensated by adjusting the turns ratio with the tappings on the high voltage side under load conditions. Potential transformers (PT) do not permit fast rising transient or high frequency voltages along with the normal supply frequency, but harmonic voltages are usually measured with sufficient accuracy. With high voltage testing transformers, no separate potential transformer is used, but a PT winding is incorporated with the high voltage windings of the testing transformer.

With test objects like insulators, cables, etc. which are capacitive in nature, a voltage rise occurs on load with the testing transformer, and the potential transformer winding gives voltage values less than the actual voltages applied to the test object. If the percentage impedance of the testing transformer is known, the following correction can be applied to the voltage measured by the PT winding of the transformer.

$$V_2 = V_{20} (1 + 0.01 v_x C/C_N) \quad (7.17)$$

where,

- $V_{20}$ = open circuit voltage of the PT winding,
- $C_N$ = load capacitance used for testing,
- $C$ = test object capacitance ($C \ll C_N$), and
- $v_x$ = % reactance drop in the transformer.

7.2.4 Electrostatic Voltmeters

Principle

In electrostatic fields, the attractive force between the electrodes of a parallel plate condenser is given by

$$F = \left| \frac{\delta W_s}{dS} \right| = \left| \frac{\delta}{\delta S} \left( \frac{1}{2} CV^2 \right) \right| = \left| \frac{1}{2} V^2 \frac{\delta C}{\delta S} \right|$$
\[ V^2 = \frac{1}{2} \varepsilon_0 \varepsilon^2 \frac{A}{s^2} = \frac{1}{2} \varepsilon_0 A \left( \frac{V}{s} \right)^2 \] (7.18)

where,

- \( V \) = applied voltage between plates,
- \( C \) = capacitance between the plates,
- \( A \) = area of cross-section of the plates,
- \( s \) = separation between the plates,
- \( \varepsilon_0 \) = permittivity of the medium (air or free space), and
- \( W_s \) = work done in displacing a plate.

When one of the electrodes is free to move, the force on the plate can be measured by controlling it by a spring or balancing it with a counter weight. For high voltage measurements, a small displacement of one of the electrodes by a fraction of a millimetre to a few millimetres is usually sufficient for voltage measurements. As the force is proportional to the square of the applied voltage, the measurement can be made for a.c. or d.c. voltages.

**Construction**

Electrostatic voltmeters are made with parallel plate configuration using guard rings to avoid corona and field fringing at the edges. An absolute voltmeter is made by balancing the plate with a counter weight and is calibrated in terms of a small weight. Usually the electrostatic voltmeters have a small capacitance (5 to 50 pF) and high insulation resistance (\( R > 10^{13} \Omega \)). Hence they are considered as devices with high input impedance. The upper frequency limit for a.c. applications is determined from the following considerations:

(i) natural frequency of the moving system,
(ii) resonant frequency of the lead and stray inductances with meter capacitance, and
(iii) the R-C behaviour of the retaining or control spring (due to the frictional resistance and elastance).

An upper frequency limit of about one MHz is achieved in careful designs. The accuracy for a.c. voltage measurements is better than ±0.25%, and for d.c. voltage measurements it may be ±0.1% or less.

The schematic diagram of an absolute electrostatic voltmeter or electrometer is given in Fig. 7.13. It consists of parallel plane disc type electrodes separated by a small distance. The moving electrode is surrounded by a fixed guard ring to make the field uniform in the central region. In order to measure the given voltage with precision, the disc diameter is to be increased, and the gap distance is to be made less. The limitation on the gap distance is the safe working stress (\( V/s \)) allowed in air which is normally 5 kV/cm or less. The main difference between several forms of voltmeters lies in the manner in which the restoring force is obtained. For conventional versions of meters, a simple spring control is used, which actuates a pointer to move on the scale of the instruments. In more versatile instruments, only small movements of the moving electrodes is allowed, and the movement is amplified through optical means (lamp and scale arrangement as used with moving coil galvanometers). Two air vane dampers are used to reduce vibrational tendencies in the moving system, and the
The constructional details of an absolute electrostatic voltmeter is given in Fig. 7.13a. The control torque is provided by a balancing weight. The moving disc M forms the central core of the guard ring G which is of the same diameter as the fixed plate F. The cap D encloses a sensitive balance B, one arm of which carries the suspension of the moving disc. The balance beam carries a mirror which reflects a beam of light. The movement of the disc is thereby magnified. As the spacing between the two electrodes is large, the uniformity of the electric field is maintained by the guard rings H which surround the space between the discs F and M. The guard rings H are maintained at a constant potential in space by a capacitance divider ensuring a uniform special potential distribution.

Some instruments are constructed in an enclosed structure containing compressed air, carbon dioxide, or nitrogen. The gas pressure may be of the order of 15 atm. Working stresses as high as 100 kV/cm may be used in an electrostatic meter in
vacuum. With compressed gas or vacuum as medium, the meter is compact and much smaller in size.

7.2.5 Peak Reading a.c. Voltmeters

In some occasions, the peak value of an a.c. waveform is more important. This is necessary to obtain the maximum dielectric strength of insulating solids, etc. When the waveform is not sinusoidal, rms value of the voltage multiplied by $\sqrt{2}$ is not correct. Hence a separate peak value instrument is desirable in high voltage applications.

**Series Capacitor Peak Voltmeter**

When a capacitor is connected to a sinusoidal voltage source, the charging current $i_0$

$$i_0 = C \int_0^t \frac{v(t) dt}{j \omega C}$$

where $v$ is the rms value of the voltage and $\omega$ is the angular frequency. If a half wave rectifier is used, the arithmetic mean of the rectifier current is proportional to the peak value of the a.c. voltage. The schematic diagram of the circuit arrangement is shown in Fig. 7.14. The d.c. meter reading is proportional to the peak value of the value $V_m$ or

$$V_m = \frac{I}{2\pi f C}$$

where $I$ is the d.c. current read by the meter and $C$ is the capacitance of the capacitor. This method is known as the Chubb-Frotscue method for peak voltage measurement.

The diode $D_1$ is used to rectify the a.c. current in one half cycle while $D_2$ by-passes in the other half cycle. This arrangement is suitable only for positive or negative half cycles.

![Fig. 7.14 Peak voltmeter with a series capacitor](image)

- **C** — Capacitor
- **$D_1, D_2$** — Diodes
- **$P$** — Protective device
- **$I$** — Indicating meter
- **$v(t)$** — Voltage waveform
- **$i_C(t)$** — Capacitor current waveform
- **$T$** — Period

(rectified current indicated)
cycles and hence is valid only when both half cycles are symmetrical and equal. This method is not suitable when the voltage waveform is not sinusoidal but contains more than one peak or maximum as shown in Fig. 7.14. The charging current through the capacitor changes its polarity within one half cycle itself. The shaded areas in Fig. 7.15 give the reverse current in any one of the half cycles and the current within that period subtracts from the net current. Hence the reading of the meter will be less and is not proportional to $V_m$ as the current flowing during the intervals $(t_1 - t_2)$ etc. will not be included in the mean value. The 'second' or the false maxima is easily spotted out by observing the waveform of the charging current on an oscilloscope. Under normal conditions with a.c. testing, such waveforms do not occur and as such do not give rise to errors. But pre-discharge currents within the test circuits cause very short duration voltage drops which may introduce errors. This problem can also be overcome by using a resistance $R$ in series with capacitor $C$ such that $CR \ll 1/\omega$ for 50 Hz application. The error due to the resistance is

$$\frac{\Delta V}{V} = \frac{V - V_m}{V} = \left(1 - \frac{1}{1 + \omega^2 C^2 R^2}\right)$$  \hspace{0.5cm} (7.19)

where,

$V =$ actual value, and

$V_m =$ measured value

![Fig. 7.15 Voltage waveform with harmonic content showing false maxima](image)

In determining the error, the actual value of the angular frequency $\omega$ has to be determined.

The different sources that contribute to the error are

(i) the effective value of the capacitance being different from the measured value of $C$

(ii) imperfect rectifiers which allow small reverse currents

(iii) non-sinusoidal voltage waveforms with more than one peak or maxima per half cycle

(iv) deviation of the frequency from that of the value used for calibration
As such, this method in its basic form is not suitable for waveforms with more than one peak in each half cycle.

A digital peak reading meter for voltage measurements is shown in Fig. 7.16. Instead of directly measuring the rectified charging current, a proportional analog voltage signal is derived which is then converted into a proportional medium frequency, \( f_m \). The frequency ratio \( f_m/f \) is measured with a gate circuit controlled by the a.c. power frequency \( f \) and a counter that opens for an adjustable number of periods \( \Delta t = pf \). During this interval, the number of impulses counted, \( n \), is

\[
n = f_m \cdot \Delta t = p \cdot \frac{f_m}{f} = 2pCV_m AR
\]

(7.20)

where \( p \) is a constant of the instrument and \( A \) represents the conversion factor of the a.c. to d.c. converter. \( A = f_m/(R i_m) \); \( i_m \) is the rectified current through resistance \( R \). An immediate reading of the voltage in kV can be obtained by suitable choice of the parameters \( R \) and the number of periods \( p \). The total estimated error in this instrument was less than 0.35%. Conventional instruments of this type are available with less than 2% error.

Peak Voltmeters with Potential Dividers

Peak voltmeters using capacitance dividers designed by Bowlder et al., are shown in Fig. 7.17a. The voltage across \( C_2 \) is made use of in charging the storage capacitor \( C_s \). \( R_d \) is a discharge resistor employed to permit variation of \( V_{m} \) whenever \( V_{m} \) is reduced. \( C_s \) is charged to a voltage proportional to the peak value to be measured. The indicating meter is either an electrostatic voltmeter or a high impedance V.T.V.M. The discharge time constant \( C_s R_d \) is designed to be about 1 to 10 s. This gives rise to a discharge error which depends on the frequency of the supply voltage. To compensate for the charging and discharging errors due to the resistances, the circuit is modified as shown in Fig. 7.17b. Measurement of the average peak is done by a microameter. Rabus’ modification to compensate the charging errors is given in Fig. 7.17c.
**Fig. 7.17a** Peak voltmeter with a capacitor potential divider and electrostatic voltmeter

\[ V_1 \quad C_1 \quad D \quad R_2 \quad C_S \quad R_d \quad V_m \]

**Fig. 7.17b** Peak voltmeter as modified by Haefeely (ref. 19)

\[ \text{Equalizing branch} \quad \frac{C_3}{R} \quad \frac{C_1}{D} \quad \frac{C_S}{R} \quad \text{Measuring branch} \]

**Fig. 7.17c** Peak voltmeter with equalizing branch as designed by Rabus

- \( M \) — Electrostatic voltmeter or V.T.V.M. of high impedance
- \( C_{S2} = C_{S1} + C \) meter
- \( R_{d2} = R_{d1} \)

Rabus (ref. 20)
7.2.6 Spark Gaps for Measurement of High d.c., a.c. and Impulse Voltages (Peak Values)

A uniform field spark gap will always have a sparkover voltage within a known tolerance under constant atmospheric conditions. Hence a spark gap can be used for measurement of the peak value of the voltage, if the gap distance is known. A sparkover voltage of 30 kV (peak) at 1 cm spacing in air at 20°C and 760 torr pressure occurs for a sphere gap or any uniform field gap. But experience has shown that these measurements are reliable only for certain gap configurations. Normally, only sphere gaps are used for voltage measurements. In certain cases uniform field gaps and rod gaps are also used, but their accuracy is less. The spark gap breakdown, especially the sphere gap breakdown, is independent of the voltage waveform and hence is highly suitable for all types of waveforms from d.c. to impulse voltages of short rise times (rise time ≥ 0.5 μs). As such, sphere gaps can be used for radio frequency a.c. voltage peak measurements also (up to 1 MHz).

Sphere Gap Measurements

Sphere gaps can be arranged either (i) vertically with lower sphere grounded, or (ii) horizontally with both spheres connected to the source voltage or one sphere grounded. In horizontal configurations, it is generally arranged such that both spheres are symmetrically at high voltage above the ground. The two spheres used are identical in size and shape. The schematic arrangement is shown in Figs. 7.18a and 7.18b. The voltage to be measured is applied between the two spheres and the distance
Fig. 7.18b Horizontal arrangement of sphere gap  
(Legend as in Fig. 7.18a)

or spacing $S$ between them gives a measure of the sparkover voltage. A series resistance is usually connected between the source and the sphere gap to (i) limit the breakdown current, and (ii) to suppress unwanted oscillations in the source voltage when breakdown occurs (in case of impulse voltages). The value of the series resistance may vary from 100 to 1000 kilo ohms for a.c. or d.c. voltages and not more than 500 $\Omega$ in the case of impulse voltages.

In the case of a.c. peak value and d.c. voltage measurements, the applied voltage is uniformly increased until sparkover occurs in the gap. Generally, a mean of about five breakdown values is taken when they agree to within ±3%.

In the case of impulse voltages, to obtain 50% flashover voltage, two voltage limits, differing by not more than 2% are set such that on application of lower limit value either 2 or 4 flashovers take place and on application of upper limit value 8 or 6 flashovers take place respectively. The mean of these two limits is taken as 50% flashover voltage. In any case, a preliminary sparkover voltage measurement is to be made before actual measurements are made.

The flashover voltage for various gap distances and standard diameters of the spheres used are given in Tables 7.3 and 7.4 respectively. The values of sparkover voltages are specified in BS : 358, IEC Publication 52 of 1960 and IS : 1876 of 1962. The clearances necessary are shown in Figs. 7.18a and 7.18b for measurements to be within ±3%. The values of $A$ and $B$ indicated in the above figures are given in Table 7.5.
Table 7.3 Peak value of sparkover voltage in kV for a.c., d.c. voltages of either polarity, and for full negative standard impulse voltages (one sphere earthed) (a) and positive polarity impulse voltages and impulse voltages with long tails (b) at temperature: 25°C and pressure: 760 torr.

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Note: The values in parentheses are for impulse voltages with long tails.
Table 7.4  Sphere gap sparkover voltages in kV (peak) in air for a.c., d.c., and impulse voltage of either polarity for symmetric sphere gaps at temperature: 20°C and pressure: 760 torr

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<td></td>
<td>(1640)</td>
<td>1730.0</td>
</tr>
<tr>
<td>90.0</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>1900.0</td>
</tr>
<tr>
<td>100.0</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>2050.0</td>
</tr>
</tbody>
</table>
Sphere Gap Construction and Assembly

Sphere gaps are made with two metal spheres of identical diameters \( D \) with their shanks, operating gear, and insulator supports (Fig. 7.18a or b). Spheres are generally made of copper, brass, or aluminium; the latter is used due to low cost. The standard diameters for the spheres are 2, 5, 6.25, 10, 12.5, 15, 25, 50, 75, 100, 150, and 200 cm. The spacing is so designed and chosen such that flashover occurs near the sparking point \( P \). The spheres are carefully designed and fabricated so that their surfaces are smooth and the curvature is uniform. The radius of curvature measured with a spherometer at various points over an area enclosed by a circle of 0.3 \( D \) around the sparking point should not differ by more than \( \pm 2\% \) of the nominal value. The surface of the sphere should be free from dust, grease, or any other coating. The surface should be maintained clean but need not be polished. If excessive pitting occurs due to repeated sparkovers, they should be smoothened. The dimensions of the shanks used, the grading ring used (if necessary) with spheres, the ground clearances, etc. should follow the values indicated in Figs. 7.18a and 7.18b and Table 7.5. The high voltage conductor should be arranged such that it does not affect the field configuration. Series resistance connected should be outside the shanks at a distance \( 2D \) away from the high voltage sphere or the sparking point \( P \).

Irradiation of sphere gap is needed when measurements of voltages less than 50 kV are made with sphere gaps of 10 cm diameter or less. The irradiation may be obtained from a quartz tube mercury vapour lamp of 40 W rating. The lamp should be at a distance \( B \) or more as indicated in Table 7.5.

Table 7.5 Clearances for Sphere Gaps

<table>
<thead>
<tr>
<th>( D ) (cm)</th>
<th>Value of ( A )</th>
<th>Value of ( B ) (min)</th>
</tr>
</thead>
<tbody>
<tr>
<td>up to 6.25</td>
<td>7 ( D )</td>
<td>9 ( D )</td>
</tr>
<tr>
<td>10 to 15</td>
<td>6 ( D )</td>
<td>8 ( D )</td>
</tr>
<tr>
<td>25</td>
<td>5 ( D )</td>
<td>7 ( D )</td>
</tr>
<tr>
<td>50</td>
<td>4 ( D )</td>
<td>6 ( D )</td>
</tr>
<tr>
<td>100</td>
<td>3.5 ( D )</td>
<td>5 ( D )</td>
</tr>
<tr>
<td>150</td>
<td>3 ( D )</td>
<td>4 ( D )</td>
</tr>
<tr>
<td>200</td>
<td>3 ( D )</td>
<td>4 ( D )</td>
</tr>
</tbody>
</table>

\( A \) and \( B \) are clearances as shown in Figs. 7.18a and 7.18b.

\( D = \) diameter of the sphere; \( S = \) spacing of the gap; and \( S/D \leq 0.5 \).

Factors Influencing the Sparkover Voltage of Sphere Gaps

Various factors that affect the sparkover voltage of a sphere gap are:

(i) nearby earthed objects,
(ii) atmospheric conditions and humidity,
(iii) irradiation, and
(iv) polarity and rise time of voltage waveforms.
Detailed investigations of the above factors have been made and analysed by Craggs and Meek\(^{(1)}\), Kuffel and Abdullah\(^{(2)}\), Kuffel\(^{(15)}\), Davis and Boulder\(^{(16)}\), and several other investigators. Only a few important factors are presented here.

(I) **Effect of nearby earthed objects**

The effect of nearby earthed objects was investigated by Kuffel\(^{(14)}\) by enclosing the earthed sphere inside an earthed cylinder. It was observed that the sparkover voltage is reduced. The reduction was observed to be

\[
\Delta V = m \log \left( \frac{B}{D} \right) + C \quad (7.21)
\]

where,

- \(\Delta V\) = percentage reduction,
- \(B\) = diameter of earthed enclosing cylinder,
- \(D\) = diameter of the spheres,
- \(S\) = spacing, and \(m\) and \(C\) are constants.

The reduction was less than 2% for \(S/D < 0.5\) and \(B/D \geq 0.8\). Even for \(S/D = 1.0\) and \(B/D \geq 1.0\) the reduction was only 3%. Hence, if the specifications regarding the clearances are closely observed the error is within the tolerances and accuracy specified. The variation of breakdown voltage with \(A/D\) ratio is given in Figs. 7.19a and b for a 50 cm sphere gap. The reduction in voltage is within the accuracy limits, if \(S/D\) is kept less than 0.6. \(A\) in the above ratio \(A/D\) is the distance from sparking point to horizontal ground plane (also shown in Fig. 7.19).

![Fig. 7.19 Influence of ground planes on sparkover voltage](image-url)
(ii) Effect of atmospheric conditions

The sparkover voltage of a spark gap depends on the air density which varies with the changes in both temperature and pressure. If the sparkover voltage is $V$ under test conditions of temperature $T$ and pressure $p$ torr and if the sparkover voltage is $V_0$ under standard conditions of temperature $T = 20^\circ\text{C}$ and pressure $p = 760$ torr, then

$$V = kV_0$$

where $k$ is a function of the air density factor $d$, given by

$$d = \frac{p}{760} \left( \frac{293}{273+T} \right)$$

(7.22)

The relationship between $d$ and $k$ is given in Table 7.6.

<table>
<thead>
<tr>
<th>$d$</th>
<th>0.70</th>
<th>0.75</th>
<th>0.80</th>
<th>0.85</th>
<th>0.90</th>
<th>0.95</th>
<th>1.0</th>
<th>1.05</th>
<th>1.10</th>
<th>1.15</th>
</tr>
</thead>
<tbody>
<tr>
<td>$k$</td>
<td>0.72</td>
<td>0.77</td>
<td>0.82</td>
<td>0.86</td>
<td>0.91</td>
<td>0.95</td>
<td>1.0</td>
<td>1.05</td>
<td>1.09</td>
<td>1.12</td>
</tr>
</tbody>
</table>

The sparkover voltage increases with humidity. The increase is about 2 to 3% over normal humidity range of 8 g/m$^3$ to 15 g/m$^3$. The influence of humidity on sparkover voltage of a 25 cm sphere gap for 1 cm spacing is presented in Fig. 7.20. It can be seen that the increase in sparkover voltage is less than 3% and the variation between a.c. and d.c. breakdown voltages is negligible (< 0.5%). Hence, it may be concluded that (i) the humidity effect increases with the size of spheres and is maximum for uniform field gaps, and (ii) the sparkover voltage increases with the partial pressure of water vapour in air, and for a given humidity condition, the change in sparkover voltage.

![Humidity vs Breakdown Voltage](image_url)

**Fig. 7.20** Influence of humidity on d.c. and a.c. breakdown voltages (25 cm dia sphere gap, 1 cm spacing)
increases with the gap length. As the change in sparkover voltage with humidity is within 3%, no correction is normally given for humidity.

(iii) Effect of Irradiation

Illumination of sphere gaps with ultra-violet or x-rays aids easy ionization in gaps. The effect of irradiation is pronounced for small gap spacings. A reduction of about 20% in sparkover voltage was observed for spacings of 0.1 \( D \) to 0.3 \( D \) for a 1.3 cm sphere gap with d.c. voltages. The reduction in sparkover voltage is less than 5% for gap spacings more than 1 cm, and for gap spacings of 2 cm or more it is about 1.5%. Hence, irradiation is necessary for smaller sphere gaps of gap spacing less than 1 cm for obtaining consistent values.

(iv) Effect of polarity and waveform

It has been observed that the sparkover voltages for positive and negative polarity impulses are different. Experimental investigation showed that for sphere gaps of 6.25 to 25 cm diameter, the difference between positive and negative d.c. voltages is not more than 1%. For smaller sphere gaps (2 cm diameter and less) the difference was about 8% between negative and positive impulses of 1/50 \( \mu \)s waveform. Similarly, the wave front and wave tail durations also influence the breakdown voltage. For wave fronts of less than 0.5 \( \mu \)s and wave tails less than 5 \( \mu \)s the breakdown voltages are not consistent and hence the use of sphere gap is not recommended for voltage measurement in such cases.

Uniform Field Electrode Gaps

Sphere gaps, although widely used for voltage measurements, have only limited range with uniform electric field. Hence, it is not possible to ensure that the sparking always takes place along the uniform field region. Rogowski (see Craggs and Meek\(^{1}\)) presented a design for uniform field electrodes for sparkover voltages up to 600 kV. The sparkover voltage in a uniform field gap is given by

\[ V = AS + B\sqrt{S} \]

where \( A \) and \( B \) are constants, \( S \) is the gap spacing in cm, and \( V \) is the sparkover voltage.

Typical uniform field electrodes are shown in Fig. 7.21. The constants \( A \) and \( B \) were found to be 24.4 and 7.50 respectively at a temperature \( T = 25^\circ C \) and pressure = 760 torr. Since the sparking potential is a function of air density, the sparkover voltage for any given air density factor \( d \) (see Eq. 7.22) is modified as

\[ V = 24.4 \, dS + 7.50 \, \sqrt{dS} \]  
(7.23)

Bruce (see Craggs et al.\(^{1}\) and Kuffel et al.\(^{2}\)) made uniform field electrodes with a sine curve in the end region. According to Bruce, the electrodes with diameters of 4.5, 9.0, and 15.0 in. can be used for maximum voltages of 140, 280, and 420 kV respectively. For the Bruce profile, the constants \( A \) and \( B \) are respectively 24.22 and 6.08. Later, it was found that with humidity the sparkover voltage increases, and the relationship for sparkover voltage was modified as

\[ V = 6.66 \, \sqrt{dS} + [24.55 + 0.41(0.1e - 1.0)]dS \]  
(7.24)

where, \( V = \) sparkover voltage, kV\(_{peak}\) (in kV\(_{d.c.}\)).
AC, EF — Flat portion (≥ S)
Curvature A to B and C to D ≥ 108
Curvature B to E and D to F
continuously increasing

AB — Flat portion
BC — Sine curve
CD — arc of a circle with centre at O
XY — \( OC \sin \left( \frac{\pi}{2} \cdot \frac{BX}{BO} \right) \)

(a) Electrodes for 300 kV (rms) spark gap
(b) Bruce profile (half contour)

Fig. 7.21 Uniform field electrode spark gap

\[ S = \text{spacing between the electrodes, cm,} \]

\[ d = \text{air density factor, and} \]

\[ e = \text{vapour pressure of water in air (mm Hg).} \]

The constants A and B differ for a.c., d.c., and impulse voltages. A comparison between the sparkover voltages (in air at a temperature of 20°C and a pressure of 760 torr) of a uniform field electrode gap and a sphere gap is given in Table 7.7. From this table it may be concluded that within the specified limitations and error limits, there is no significant difference among the sparkover voltages of sphere gaps and uniform field gaps.

**Table 7.7 Sparkover Voltages of Uniform Field Gaps and Sphere Gaps at \( t = 20°C \) and \( p = 760 \text{ torr} \)**

<table>
<thead>
<tr>
<th>Gap spacing (cm)</th>
<th>Sparkover voltage with uniform field electrodes as measured by</th>
<th>Sphere gap sparkover voltage (kV)</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td>Ritz (kV)</td>
<td>Bruce (kV)</td>
</tr>
<tr>
<td>0.1</td>
<td>4.54</td>
<td>4.50</td>
</tr>
<tr>
<td>0.2</td>
<td>7.90</td>
<td>7.56</td>
</tr>
<tr>
<td>0.5</td>
<td>17.00</td>
<td>16.41</td>
</tr>
<tr>
<td>1.0</td>
<td>31.35</td>
<td>30.30</td>
</tr>
<tr>
<td>2.0</td>
<td>58.70</td>
<td>57.04</td>
</tr>
<tr>
<td>4.0</td>
<td>112.00</td>
<td>109.00</td>
</tr>
</tbody>
</table>
The sparkover voltage of uniform field electrode gaps can also be found from calculations. However, no such calculation is available for sphere gaps. In spite of the superior performance and accuracy, the uniform field spark gap is not usually used for measurement purposes, as very accurate finish of the electrode surfaces and careful alignment are difficult to obtain in practice.

**Rod Gaps**

A rod gap is also sometimes used for approximate measurement of peak values of power frequency voltages and impulse voltages. IEEE recognise that this method gives an accuracy within ±8%. The rods will be either square edged or circular in cross-section. The length of the rods may be 15 to 75 cm and the spacing varies from 2 to 200 cm. The sparkover voltage, as in other gaps, is affected by humidity and air density. The power frequency breakdown voltage for 1.27 cm square rods in air at 25°C and at a pressure of 760 torr with the vapour pressure of water of 15.5 torr is given in Table 7.8. The humidity correction is given in Table 7.9. The air density correction factor can be taken from Table 7.6.

**Table 7.8 Sparkover Voltage for Rod Gaps**

<table>
<thead>
<tr>
<th>Gap spacing (cm)</th>
<th>Sparkover voltage (kV)</th>
<th>Gap spacing (cm)</th>
<th>Sparkover voltage (kV)</th>
</tr>
</thead>
<tbody>
<tr>
<td>2</td>
<td>26</td>
<td>30</td>
<td>172</td>
</tr>
<tr>
<td>4</td>
<td>47</td>
<td>40</td>
<td>225</td>
</tr>
<tr>
<td>6</td>
<td>62</td>
<td>50</td>
<td>278</td>
</tr>
<tr>
<td>8</td>
<td>72</td>
<td>60</td>
<td>332</td>
</tr>
<tr>
<td>10</td>
<td>81</td>
<td>70</td>
<td>382</td>
</tr>
<tr>
<td>15</td>
<td>102</td>
<td>80</td>
<td>435</td>
</tr>
<tr>
<td>20</td>
<td>124</td>
<td>90</td>
<td>488</td>
</tr>
<tr>
<td>25</td>
<td>147</td>
<td>100</td>
<td>537</td>
</tr>
</tbody>
</table>

The rods are 1.27 cm square edged at \( t = 27°C \), \( p = 760 \) torr, and vapour pressure of water = 15.5 torr.

**Table 7.9 Humidity Correction for Rod Gap Sparkover Voltages**

<table>
<thead>
<tr>
<th>Vapour pressure of water (torr)</th>
<th>Correction factor %</th>
</tr>
</thead>
<tbody>
<tr>
<td>2.54</td>
<td>-16.5</td>
</tr>
<tr>
<td>5</td>
<td>-13.1</td>
</tr>
<tr>
<td>10</td>
<td>-6.5</td>
</tr>
<tr>
<td>15</td>
<td>-0.5</td>
</tr>
<tr>
<td>20</td>
<td>4.4</td>
</tr>
<tr>
<td>25</td>
<td>7.9</td>
</tr>
<tr>
<td>30</td>
<td>10.1</td>
</tr>
</tbody>
</table>
In case of impulse voltage measurements, the IEC and IEEE recommend horizontal mounting of rod gaps on insulators at a height of 1.5 to 2.0 times the gap spacing above the ground. One of the rods is usually earthed. For 50% flashover voltages, the procedure followed is the same as that for sphere gaps. Corrections for humidity for 1/50 μs impulse and 1/50 μs impulse waves of either polarity are given in Fig. 7.22. The sparkover voltages for impulse waves are given in Table 7.10.

### Table 7.10 Sparkover Voltages of Rod Gaps for Impulse Voltages at Temperature = 20°C, Pressure = 760 torr and Humidity = 11 g/cm²

<table>
<thead>
<tr>
<th>Gap length (cm)</th>
<th>1/5 μs wave (kV) Positive</th>
<th>1/5 μs wave (kV) Negative</th>
<th>1/50 μs wave (kV) Positive</th>
<th>1/50 μs wave (kV) Negative</th>
</tr>
</thead>
<tbody>
<tr>
<td>5</td>
<td>60</td>
<td>66</td>
<td>56</td>
<td>61</td>
</tr>
<tr>
<td>10</td>
<td>101</td>
<td>111</td>
<td>90</td>
<td>97</td>
</tr>
<tr>
<td>20</td>
<td>179</td>
<td>208</td>
<td>160</td>
<td>178</td>
</tr>
<tr>
<td>30</td>
<td>256</td>
<td>301</td>
<td>226</td>
<td>262</td>
</tr>
<tr>
<td>40</td>
<td>348</td>
<td>392</td>
<td>279</td>
<td>339</td>
</tr>
<tr>
<td>50</td>
<td>431</td>
<td>475</td>
<td>334</td>
<td>407</td>
</tr>
<tr>
<td>60</td>
<td>513</td>
<td>557</td>
<td>397</td>
<td>470</td>
</tr>
<tr>
<td>80</td>
<td>657</td>
<td>701</td>
<td>511</td>
<td>585</td>
</tr>
<tr>
<td>100</td>
<td>820</td>
<td>855</td>
<td>629</td>
<td>703</td>
</tr>
</tbody>
</table>
7.2.7 Potential Dividers for Impulse Voltage Measurements

Potential or voltage dividers for high voltage impulse measurements, high frequency a.c. measurements, or for fast rising transient voltage measurements are usually either resistive or capacitive or mixed element type. The low voltage arm of the divider is usually connected to a fast recording oscillograph or a peak reading instrument through a delay cable. A schematic diagram of a potential divider with its terminating equipment is given in Fig. 7.23. \( Z_1 \) is usually a resistor or a series of resistors in case of a resistance potential divider, or a single or a number of capacitors in case of a capacitance divider. It can also be a combination of both resistors and capacitors. \( Z_2 \) will be a resistor or a capacitor or an \( R-C \) impedance depending upon the type of the divider. Each element in the divider, in case of high voltage dividers, has a self-resistance or capacitance. In addition, the resistive elements have residual induc-tances, a terminal stray capacitance to ground, and terminal to terminal capacitances.

![Schematic diagram of a potential divider with a delay cable and oscilloscope](image)

The lumped-circuit equivalent of a resistive element is already shown in Fig. 7.7, and the equivalent circuit of the divider with inductance neglected is of the form shown in Fig. 7.8a. A capacitance potential divider also has the same equivalent circuit as in Fig. 7.7a, where \( C \) will be the capacitance of each elemental capacitor, \( C_g \) will be the terminal capacitance to ground, and \( R \) will be the equivalent leakage resistance and resistance due to dielectric loss in the element. When a step or fast rising voltage is applied at the high voltage terminal, the voltage developed across the element \( V \) will not have the true waveform as that of the applied voltage. The cable can also introduce distortion in the waveshape. The following elements mainly constitute the different errors in the measurement:

(i) residual inductance in the elements;
(ii) stray capacitance occurring
(a) between the elements,
(b) from sections and terminals of the elements to ground, and
(c) from the high voltage lead to the elements or sections;
(iii) the impedance errors due to
(a) connecting leads between the divider and the test objects, and
(b) ground return leads and extraneous current in ground leads; and
(iv) parasitic oscillations due to lead and cable inductances and capacitance of high voltage terminal to ground.

The effect to residual and lead inductances becomes pronounced when fast rising impulses of less than one microsecond are to be measured. The residual inductances damp and slow down the fast rising pulses. Secondly, the layout of the test objects, the impulse generator, and the ground leads also require special attention to minimize recording errors. These are discussed in Sec. 7.4.

**Resistance Potential Divider for Very Low Impulse Voltages and Fast Rising Pulses**

A simple resistance potential divider consists of two resistances $R_1$ and $R_2$ in series ($R_1 \gg R_2$) (see Fig. 7.24). The attenuation factor of the divider or the voltage ratio is given by

$$a = \frac{V_1(t)}{V_2(t)} = 1 + \frac{R_1}{R_2}. \quad (7.25)$$

The divider element $R_2$, in practice, is connected through the coaxial cable to the oscilloscope. The cable will generally have a surge impedance $Z_0$ and this will come in parallel with the oscilloscope input impedance ($R_m$, $C_m$). $R_m$ will generally be greater than one megaohm and $C_m$ may be 10 to 50 picofarads. For high frequency and impulse voltages (since they also contain high frequency fundamental and harmonics), the ratio in the frequency domain will be given by

$$a = \frac{V_1}{V_2} = 1 + \frac{R_1}{(R_2/1 + j\omega R_2 C_m)} \quad (7.26)$$

Hence, the ratio is a function of the frequency. To avoid the frequency dependance of the voltage ratio $a$, the divider is compensated by adding an additional capacitance $C_1$ across $R_1$. The value of $C_1$, to make the divider independent of the frequency, may be obtained from the relation,
Fig. 7.24c Output of compensated resistance voltage divider for different degrees of compensation

\[
\frac{R_1}{R_2} = \frac{C_m}{C_1} \tag{7.27}
\]

or,

\[R_1C_1 = R_2C_m\]

meaning that the time constant of both the arms should be the same. This compensation is used for the construction of high voltage dividers and probes used with oscilloscopes. Usually, probes are made with adjustable values of \(C_m\) so that the value of \(C_m\) can include any stray capacitance including that of a cable, etc. A typical high voltage probe with a four nanosecond rise time rated for 40 kV (peak) has an input impedance of 100 MΩ in parallel with 2.7 pF. The output waveforms of a compensated divider are shown in Fig. 7.24c with over and under compensation for a square wave input. In Fig. 7.24 c(i) is shown the waveform of an R-C divider when \(C_1\) is too large or overcompensated, while in Fig. 7.24 c(iii) is shown the waveform when \(C_1\) is small or under compensated. For the exponential slope or for the rising portion of the wave, the time constant \(\tau = \frac{1}{R_1R_2/(R_1 + R_2)}(C_1 + C_m)\). This will be too large when the value of \(C_1\) is greater than that required for correct compensation, i.e. \(R_1C_1 = R_2C_m\) and hence an overshoot with an exponential decay occurs as shown in Fig. 7.24 c(i). For under compensation, the charging time is too high and as such an exponential rise occurs as shown in Fig. 7.24 c(iii). The schematic circuit of a compensated oscilloscope probe is shown in Fig. 7.25.

**Potential Dividers Used for High Voltage Impulse Measurements**

In a resistance potential divider, \(R_1\) and \(R_2\) are considered as resistors of small dimensions in the previous section. For voltages above 100 kV, \(R_1\) is no longer small in dimension and is usually made of a number of sections. Hence the divider is no longer a small resistor of lumped parameters, but has to be considered as an equivalent distributed network with its terminal to ground capacitances and inter-sectional series capacitances as shown in Fig. 7.26. The total series resistance \(R_1\) is made of \(n\) resistors of value \(R'\) and \(R = nR'\). \(C_g\) is the terminal to ground capacitance of each of the
Fig. 7.25  Schematic circuit arrangement of a cathode ray oscilloscope voltage divider probe

resistor elements $R_1$, and $C_5$ is the capacitance between the terminals of each section. The inductance of each element ($L_1$) is not shown in the figure as it is usually small compared to the other elements (i.e. $R_1$, $C_s$ and $C_g$). This type of divider produces a non-linear voltage distribution along its length and also acts like an $R-C$ filter for applied voltages. The output of such a divider for various values of $C_g/C_s$ ratio is shown in Fig. 7.27 for a step input. By arranging guard rings at various elemental points, the equivalent circuit can be modified as shown in Fig. 7.28, where $C_h$
Capacitance Voltage Dividers

Capacitance voltage dividers are ideal for measurement of fast rising voltages and pulses. The capacitance ratio is independent of the frequency, if their leakage resistance is high enough to be neglected. But usually the dividers are connected to the source voltage through long leads which introduce lead inductances and residual resistances. Also, the capacitance used for very high voltage work is not small in dimension and hence cannot be considered as a lumped element. Therefore, the output of the divider for high frequencies and impulses is distorted as in the case of resistance dividers.

Pure Capacitance Dividers

A pure capacitance divider for high voltage measurements and its electrical equivalent network without stray elements is shown in Fig. 7.29. The ratio of the divider

$$a = \frac{V_1(t)}{V_2(t)} = 1 + \frac{C_2}{C_1}$$  \hspace{1cm} (7.28)

Capacitance $C_1$ is formed between the h.v. terminal of the source (impulse generator) and that of the test object or any other point of measurement. The CRO is located within the shielded screen surrounding capacitance $C_2$. $C_2$ includes the capacitance used, the lead capacitance, input capacitance of the CRO, and other
Fig. 7.29  Capacitance voltage divider for very high voltages and its electrical equivalent circuit

ground capacitances. The advantage of this connection is that the loading on the source is negligible; but a small disturbance in the location of $C_2$ or h.v. electrode or the presence of any stray object nearby changes the capacitance $C_1$, and hence the divider ratio is affected.

In many cases a standard air or compressed gas capacitor is used which has coaxial cylindrical construction. Accurate ratios that could be calculated up to 1000 : 1 have been achieved for a maximum impulse voltage of 350 kV, and the upper frequency limit is about 10 MHz. For smaller or moderate high voltages (up to 100 kV) capacitance dividers are built with an upper frequency limit of 200 MHz.

Another type of design frequently used is to make $C_1$ to consist of a number of capacitors $C_i$ in series for the given voltage $V_1$. In such cases the equivalent circuit is similar to that of a string insulator unit used in transmission lines (Fig. 7.30). The voltage distribution along the capacitor chain is non-linear and hence causes distribution of the output wave. But the ratio error is constant and is independent on frequency as compared to resistance dividers. A simplified equivalent circuit is shown in Fig. 7.30 b, which can be used if $C_1 \ll C_2$ and $C_g \ll C_1$. The voltage ratio is

$$a = \frac{V_{1}(t)}{V_{2}(t)} = \left[ 1 + \frac{C_2}{C_1} \right] \left[ 1 + \frac{C_g}{6C_1} \right]$$

This ratio is constant and gives an error of less than 5% when $C_1 = 3C_g$. This equivalent circuit is quite satisfactory up to 1 MHz.

**Field Controlled Voltage Dividers**

The electrostatic or capacitive field distribution of a shield or guard ring placed over a resistive divider to enforce a uniform field in the neighbourhood and along the divider may be adopted for high voltage measurements. The schematic diagram is shown in Fig. 7.31 and its equivalent circuit is same as that given in Fig. 7.28. The shield is of the form of a cone. $R_1$ is a non-linear resistance in the sense the resistance per unit length is not the same but is variable. The main advantage is that the
capacitance per unit length is small and hence loading effect is reduced. Sometimes the parallel resistance $R_2$ together with the lead inductance and shunt capacitances cause oscillations as shown in Fig. 7.32a. The oscillations can be reduced by adding a damping resistance $R_d$ as shown in Fig. 7.31. Such dividers are constructed for very

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**Fig. 7.30** Capacitance voltage divider with distributed network and its equivalent circuit

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**Fig. 7.31** Field controlled resistance divider with a damping resistor

- $R_d$ — Damping resistor
- $L$ — Lead inductance
- $C_p$ — Capacitance of the shield to ground
- $S$ — Shield
high voltages (up to 2 MV) with response times less than 30 ns. The resistance column, $R_1$ is made of woven resistance of 20 kilo ohms. The step response of such a divider is shown in Fig. 7.32, with and without a damping resistor. With a proper damping resistor ($R_d$) the response time is much less and the overshoot is reduced.

**Mixed R-C Potential Dividers**

Mixed potential dividers use $R$-$C$ elements in series or in parallel. One method is to connect capacitance in parallel with each $R'$ element. This is successfully employed with low inductance with short lead of 14' long

\[
R_1 = \frac{R_2}{n}
\]

\[
C' = nC_1 \left[ 1 - \frac{C_g}{6C_1} \right]
\]

\[
C_g = \text{ground capacitance}
\]

\[
C_1 = \text{total series capacitance}
\]

\[
R_1 = \text{total resistance}
\]

\[
R_1 C_1 = R_2 C_2
\]

(b) Step response determined with low voltage step pulse

![Fig. 7.33 Equivalent circuit of a series R-C voltage divider and its step response](image-url)
for voltage dividers of rating 2 MV and above. A better construction is to make an R-C series element connection. The equivalent circuit of such a construction is shown in Fig. 7.33. Such dividers are made for 5 MV with response times less than 30 ns. The low voltage arm $R_2$ is given "L peaking" by connecting a variable inductance $L$ in series with $R_2$. The step response of the divider and the schematic connection of low voltage arm are shown in Fig. 7.34. However, for a correctly designed voltage divider $L$ peaking will not be necessary.

![Fig. 7.34 L peaking in low voltage arm and step response of the divider with L peaking](image)

**R-C Potential dividers for 2 MV rating and above**

Voltage dividers used for measuring more than one million volts attenuate the measuring signal to a value in the range 100 V to few hundreds of volts. The criteria required to assess the dividers are: (i) the shape of the voltage in the test arrangement should be transferred without any distortion to the L.V. side, (ii) simple determination of transfer behaviour should be ensured, and (iii) they should be suitable for multipurpose use, i.e. for use with a.c. power frequency voltages, switching impulse voltages as well as with lightning impulse voltages. This condition necessitates that the dividers should have broad bandwidths. The above requirements are generally met by (a) optimally damped R-C dividers, or (b) under damped or low damped R-C dividers. The high voltage arm of such dividers consists of series R-C units while the secondary arm is usually an R-C series or parallel circuit. In case of optimally damped dividers, $R_1 = 4\sqrt{L_1/C_g}$, where $L_1$ is the inductance of the high voltage lead and the H.V. portion of the divider, and $C_g$ is the equivalent capacitance to ground. Usually this resistance will be 400 to 1000 ohms. On the other hand, for low or underdamped dividers, $R_1$ will be equal to 0.25 to 1.5 times $\sqrt{L/C_1}$ where $L$ is the inductance for the complete measuring loop and $C_1$ is the capacitance of the H.V. part of the divider. In this case, the normal value of $R_1$ lies between 50 and 300 ohms. The step response of the two types of dividers mentioned above is shown in Fig. 7.35. In actual practice, because of the large time constant $(R_d + R_1)C_1$, the optimal damped divider affects the voltage shape at the test object. Standard lightning impulses sometimes cannot be generated to the correct standard specifications. As such, R-C potential dividers are
(i) Optimally damped

Response time : 50 n sec  
Front time : 50 n sec  
Overshoot : = 3%  
Parameters : \( R_1 = 1000 \, \Omega \)  
\[ C_1 = 360 \, \text{pF} \]

Damping resistance \( = 500 \, \Omega \)
\[
(R_d + R_1) C_1 = R_2 C_2
\]

(ii) Underdamped

Response time : 4 n sec  
Front time : 110 n sec  
Overshoot : = 30%  
Parameters : \( R_1 = 256 \, \Omega \)  
\[ C_1 = 400 \, \text{pF} \]

Damping resistance : 0 \( \Omega \)
\[
R_1 C_1 = R_2 C_2
\]

Fig. 7.35 Step response of a 4 MV R-C divider
not suitable for measurements with test objects of very low capacitance. The low or underdamped R-C divider acts as a load capacitance and a voltage divider, and is suitable for applications over a broad bandwidth, i.e. a.c., switching impulses, lightning impulses, chopped waves etc. Underdamped R-C dividers are also suitable for measurement of steep fronted impulse waves. A typical record of lightning impulse wave (1.2/50 µs wave) obtained using both the above types of dividers is shown in Fig. 7.36. It may be noted that even though the step response is poor in the case of underdamped dividers, they can be used to measure the standard impulse wave to a better accuracy.

**Different Connections Employed with Potential Dividers**

Different arrangements and connections of voltage or potential dividers with a cathode ray oscilloscope are shown in Figs. 7.37 and 7.38.

A simple arrangement of a resistance divider is shown in Fig. 7.37 a. The possible errors are (i) $R_2 \neq Z_0$ (surge impedance of the cable), (ii) capacitance of the cable and CRO shunting $R_2$ and hence introducing distortion, (iii) attenuation or voltage drop in surge cable $Z_0$, and (iv) ground capacitance effect. These errors are already discussed in Sec. 7.2.7. To avoid reflections at the junction of the cable and $R_2$, $R_2$ is varied and adjusted to give the best possible step response. When a unit function voltage is applied to the circuit shown in Fig. 7.37 b, the effect of the cable is to take a fraction of the voltage $[C_1/(C_1 + C_2)]$ into it and cause reflections at the input end. In the beginning the cable acts like a resistance of value $= Z_0$ the surge impedance, but later behaves like a capacitor of value equal to the total capacitance of the cable. This behaviour introduces distortion and is compensated by using a split capacitor connection as shown in Fig. 7.37 c with $(C_1 + C_2) = (C_3 + C_k) \quad [C_k = \text{capacitance of the cable}].$ On the other hand if $C_d/(C_1 + C_2 + C_k) = 0.1$, the error will be less than 1.5%.
(a) Resistance potential divider with surge cable and CRO

(b) Capacitance divider with surge cable and CRO

(c) Split capacitor arrangement $R = Z_0$

(d) Resistance potential divider with surge cable and CRO.
Voltage ratio, $V_1/V_2 = 1 + (R_1/R_2) + (R_1/R_2')$ where $R_2'/Z_0$

Fig. 7.37 Potential divider arrangements
The arrangements for mixed potential dividers are shown in Fig. 7.38. The arrangement shown in Fig. 7.38a is modified and improved in the arrangement of Fig. 7.38b. With

\[
R_1 C_1 = \frac{C_2 Z_0 (C_1 + C_2 + C_3)}{(C_1 + C_2)} \quad (7.30)
\]

\[
Z_0 = R_3 + \left( \frac{R_1 R_2}{R_1 + R_2} \right), \quad \text{and} \quad R_1 C_1 = R_2 C_3 \quad (7.31)
\]

the response is greatly improved. The arrangement shown in Fig. 7.38c is simple and gives the desired impedance matching.

\[ R = Z_0 \]

The mode of connection and the layout arrangement of the secondary arm of the divider is very critical for the distortionless measurement of fast transients. The L.V. arm of the divider itself introduces large distortions if not properly connected. Different corrections employed for connecting the L.V. arm to the measuring instrument via the signal cables are shown in Figs. 7.37 and 7.38. The signal cable \( Z_0 \) may

**Fig. 7.38** Mixed potential divider arrangements

**Low voltage arms of the measuring system connected to voltage dividers**

The mode of connection and the layout arrangement of the secondary arm of the divider is very critical for the distortionless measurement of fast transients. The L.V. arm of the divider itself introduces large distortions if not properly connected. Different corrections employed for connecting the L.V. arm to the measuring instrument via the signal cables are shown in Figs. 7.37 and 7.38. The signal cable \( Z_0 \) may
be assumed to be loss-free so that the surge impedance, $Z_0 = \sqrt{L/C}$ is independent of the frequency and the travel time for the signal, $T_0 = \sqrt{L/C}$ (refer to Chapter 8 for details). In the case of resistance dividers, the cable matching is achieved by having a pure resistance, $R_2 = Z_0$ at the end of the cable. The surge cable $Z_0$ and the resistance $R_2$ form an integral part of the cable system. Typically, $Z_0$ has values of 50 or 75 ohms. In actual practice, signal cables do have losses due to skin effect at high frequencies and hence $Z_0$ becomes a complex quantity. Thus, the matching of $R_2$ with $Z_0$ should be done at high frequencies or with a step input as indicated earlier. In the case of long cables, the cable resistance including that of the shield wire should be taken as a part of the matching resistance. The divider ratio in the case of the connection shown in Fig. 7.37 is

$$a = \frac{V_1}{V_2} = 1 + \frac{R_1}{R_2} + \frac{R_1}{R_2'}$$

and

$$R_2' = Z_0$$

(7.32)

For the capacitance dividers, the signal cable cannot be completely matched. A low ohmic resistance connected in parallel with $C_2$ would load the L.V arm and hence, the output gets decreased. Connection of a resistance $R = Z_0$ at the input end (see figures 7.37 and 7.38) will make the voltage across the CRO the same as that across $C_2$. The transient voltage ratio, at $t = 0$ is given as

$$a = \frac{V_1}{V_2} = 1 + \frac{C_2}{C_1}$$

and

effective $= 1 + \frac{(C_2 + C_k)}{C_1}$ for $t >> 2T_0$

(7.33)

Where $C_k$ is the cable capacitance.

Thus, an initial overshoot of $\Delta V = C_k/(C_1 + C_2)$ will appear. This will be either small or negligible for medium and low cable lengths, and for high values of capacitance $C_2$. This error can be avoided and the response improved in the case of $R-C$ dividers by using the arrangements shown in Fig. 7.38.

![Fig. 7.39 L.V. arm layout for voltage dividers](image-url)
Usually, the L.V. arms are made co-axial and are enclosed in metal boxes that are solidly grounded. The series resistors used in R-C divider forms an integral part of the divider’s L.V. arm. Further, all the L.V. arm capacitors and inductors should have a very low inductance. A typical L.V. arm arrangement is shown in Fig. 7.39.

7.2.8 Peak Reading Voltmeters for Impulse Voltages

Sometimes it is enough if the peak value of an impulse voltage wave is measured; its waveshape might already be known or fixed by the source itself. This is highly useful in routine impulse testing work. The methods are similar to those employed for a.c. voltage crest value measurements. The instrument is normally connected to the low voltage arms of the potential dividers described in Sec. 7.2.7. The basic circuit along with its equivalent circuit and the response characteristic is shown in Fig. 7.40. The circuit consists of only valve rectifiers.

Diode D conducts for positive voltages only. For negative pulses, the diode has to be connected in reverse. When a voltage impulse \( v(t) \) appears across the low voltage arm of the potential divider, the capacitor \( C_m \) is charged to the peak value of the pulse. When the amplitude of the signal starts decreasing the diode becomes reverse biased and prevents the discharging of the capacitor \( C_m \). The voltage developed across \( C_m \) is measured by a high impedance voltmeter (an electrostatic voltmeter or an electrometer). As the diode D has finite forward resistance, the voltage to which \( C_m \) is charged will be less than the actual peak of the signal, and is modified by the \( R-C \) network of the diode resistance and the measuring capacitance \( C_m \). The error is shown in Fig. 7.40c. The error can be estimated if the waveform is known. The actual forward

![Diagram](image-url)
resistance of the diode D (dynamic value) is difficult to estimate, and hence the meter is calibrated using an oscilloscope. Peak voltmeters for either polarity employing resistance dividers and capacitance dividers are shown in Fig. 7.41. In this arrangement, the voltage of either polarity is transferred into a proportional positive measuring signal by a resistive or capacitive voltage divider and a diode circuit. An active network with feedback circuit is employed in commercial instruments, so that the fast rising pulses can also be measured. Instruments employing capacitor dividers require discharge resistance across the low voltage arm to prevent the build-up of d.c. charge.

![Fig. 7.41 Peak reading voltmeter for either polarity with (a) resistance divider, and (b) capacitance divider](image)

Low ohmic shunt with a millivoltmeter for current measurement

\[ V = IR \]

Four terminal shunt

(In 3 terminal construction the bottom C and P terminals are made common)

![Fig. 7.42 Calibrated ohmic shunt for d.c. current measurements](image)